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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

NONE  
CME

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

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CME

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 18	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after				
Verified and Acknowledged <i>C. A. Lawrence</i> Examiner's Signature	Initials			

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## TITLE

Method of designing low-power semiconductor integrated circuit

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RECEIVED 770		<input type="checkbox"/> 1.16 Fees ( Filing )
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